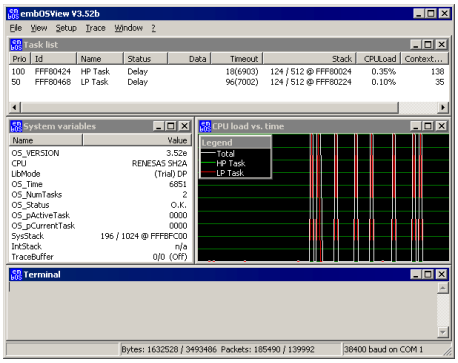
For immediate Release

SEGGER embOS supports Renesas SH2A architecture

Hilden, Germany – January 4, 2008 – SEGGER Microcontroller, a manufacturer of middleware for embedded systems announced today the availability of an embOS port for the Renesas SH2A.

“The powerful Renesas SH2A based devices show their full performance when they are combined with a small footprint Operating System that is optimized for speed, such as embOS”, said Robert Teufel, COO at SEGGER. “Deeply embedded systems frequently require maximum performance and minimum memory usage, which can only be achieved with minimum overhead in the RTOS.”



While the embOS kernel only needs 2,4 kBytes of program memory and 49 bytes of SRAM, it offers a premium set of features for the embedded world. To list just a few, the profiler embOSView, unlimited number of tasks, no need for pre-configuration and no any assembly language required. Specific support for the SH2A architecture is implemented as embOS uses the register banks of the core to speed up interrupt processing. It supports fully nested interrupts and zero interrupt latency. Important for high volume applications is the fact that SEGGER does not charge royalties for embOS.

SEGGER is a Gold Partner in the Renesas Alliance Program and supports most Renesas microcontroller-architectures with the operating system embOS and board support packages. Example software for graphic intensive applications is also available for multiple Renesas Starter Kits. These examples are based on emWin, the unique SEGGER Graphics Software, specifically developed for embedded needs.

About SEGGER

SEGGER Microcontroller GmbH&Co. KG develops and distributes hardware and software development tools. All software components are ANSI "C" compliant and can be used in embedded systems including industries such as telecom, medical technology, consumer electronics, automotive industry and industrial automation. SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB device stack) and embOS/IP (TCP/IP stack). Besides the highly efficient software products, SEGGER also provides embedded hardware tools such as the well-known J-TAG emulator J-Link, J-Trace and the Flasher (stand alone programmer). SEGGER’s intention is to cut software development time for embedded applications by offering affordable, flexible and easy-to-use middleware allowing developers to focus on their applications.

For further information please visit: http://www.segger.com

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