J-Link support added to IAR and Keil 8051 toolchains

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SEGGER's J-Link enables super-fast, direct debugging on Silicon Laboratories' (SiLabs) EFM8 (8051-core) devices. This is now supported by both market-leading toolchains, Keil's PK51 and IAR's Embedded Workbench. The J-Link's debug speed far surpasses all previously available solutions for 8051.

Connection is established via the C2 2-wire interface, a proprietary debug interface defined by SiLabs. Very high speed is true for downloading in both RAM and flash, as well as debugging.

"It shows that our J-Links are also very valuable on smaller MCUs such as EFM8," says Alex Gruener, product manager J-Link at SEGGER Microcontroller. "The debug probes deliver the same benefits to the EFM8 as they have long proven in 32-bit architectures, such as ease of use, high performance and direct download to flash memory."

EFM8 support is included in all current J-Link models and can be used without purchasing any additional license.

More information on J-Link support for SiLabs EFm8 devices is available at: https://www.segger.com/jlink-silabs-efm8-support.html

About J-Link

The SEGGER J-Link is the most popular family of debug probes on the market. It is tool chain independent and works with free GDB-based tool chains such as emIDE and Eclipse, as well as commercial IDEs from: Atmel, Atollic, Coocox, Cosmic, Freescale, IAR, KEIL, Mentor Graphics, Microchip, Python, Rowley, Renesas, Tasking and others. With the J-Link family, investments in the debug probe are preserved when changing compiler or even CPU architecture.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M, Cortex-R, Cortex-A as well as Renesas RX100, RX200, RX600 and Microchip PIC32; there is no need to buy a new J-Link or new license when switching to a different yet supported CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link family. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher-end model is a matter of a simple plug-and-play.

Full product specifications are available at: http://segger.com/jlink.html

The J-Link-Software is available at: http://segger.com/download_jlink.html

U.S. On-Line Web Shop: http://shop-us.segger.com

Online Shop (Europe, Asia, Africa): http://shop.segger.com

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About SEGGER

SEGGER Microcontroller develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical

instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER cuts software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at http://www.segger.com.

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