

## SEGGER J-Link – First Debug Probe supporting Infineon’s Single Pin Debug Interface

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SEGGER has added support for Infineon’s Single Pin Debug (SPD) Interface for the XMC1000-series of Infineon Technologies to the J-Link family of debug probes.

The J-Link is the only commercial debug probe in the market capable of connecting to a device with the SPD-interface.

“Support for Infineon’s SPD-interface makes J-Link even more versatile. It puts the full J-Link family yet another step ahead of the competition”, says Ivo Geilenbruegge, Managing Director of SEGGER.

“We are making sure our complete line of J-Link debug probes support all major tool vendors, CPU architectures and target interfaces. Being the first vendor supporting Infineon’s XMC1000 series single-wire debug interface is just one more point of proof”, says Alexander Gruener, Product Manager for J-Link.

“The SEGGER support for the SPD-interface significantly improves the eco-system for the XMC1000-series. By working with SEGGER, the SPD-interface is now accessible from all popular tool-chains in the market, including the free DAVE™ development platform and other free GDB-based development environments,” says Dr. Stephan Zizala, Senior Director, Industrial and Multimarket Microcontrollers at Infineon Technologies AG.

More details on the J-Link’s performance can be found at:  
<http://www.segger.com/jlink-flash-download.html>



### About J-Link

The SEGGER J-Link is the most popular debug probe on the market. It is tool chain independent and works with commercial IDEs from: Atmel, Atollic, Coocox, Freescale, IAR, i-Systems, ImageCraft, KEIL, Mentor Graphics, Phytion, Rowley, Renesas, Tasking and others, as well as free GDB-based tool chains such as emIDE and EmBlocks. With the J-Link family, investments in the debug probe are likely preserved when changing compiler or even CPU architecture.

J-Link supports multiple CPU families, such as ARM 7, 9, 11, Cortex-M0, M0+, M1, M3, M4, R4, A5, A8, A9 as well as Renesas RX610, 620, 62N, 62T, 630, 631, 63N; there is typically no need to buy a new J-Link or new license when switching to a different CPU family or tool-chain. SEGGER is also continuously adding support for additional cores, which in most cases, only requires a software/firmware update. Unlimited free updates are included with even the baseline model of the J-Link. SEGGER is excited to continue advanced development of its cutting edge embedded tool solutions to be utilized with pretty much any development environment you choose. All J-Links are fully compatible to each other, so an upgrade from a lower-end model to a higher-end model is a matter of a simple plug-and-play.

Different architectures, same debug probe!

Full product specifications are available at: <http://www.segger.com/jlink.html>

The J-Link-Software is available at: [http://www.segger.com/download\\_jlink.html](http://www.segger.com/download_jlink.html)

U.S. On-Line Web Shop: <http://shop-us.segger.com>

Online Shop (Europe, Asia, Africa): <http://shop.segger.com>



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## About SEGGER

**SEGGER Microcontroller** develops and distributes hardware and software development tools as well as software components for embedded systems. An "embedded system" is one in which a microprocessor and associated components are incorporated into a device helping to accomplish difficult and complex tasks in products such as cell phones, medical instruments, instrument clusters, measurement instruments, satellite radios, digital cameras etc.

SEGGER was founded in 1997, is privately held, and is growing steadily. Based in Hilden with distributors in all continents and a local office in Massachusetts, SEGGER offers its full product range worldwide.

SEGGER software products include: embOS (RTOS), emWin (GUI), emFile (File System), emUSB (USB host and device stack) and embOS/IP (TCP/IP stack). With the experience in programming efficiently on embedded systems, SEGGER created highly integrated, cost-effective programming and development tools, such as the Flasher (stand-alone flash programmer) and the industry leading J-Link/J-Trace emulator.

SEGGER's intention is to cut software development time for embedded applications by offering affordable, high quality, flexible and easy-to-use tools and software components allowing developers to focus on their applications. Find out more at <http://www.segger.com>.

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